Docket No.: IRDM.002A/IDC-0023



## INFORMATION DISCLOSURE STATEMENT

Clarence Chui

App. No

10/772,120

Filed

February 3, 2004

For

DRIVER VOLTAGE ADJUSTER

Examiner

Unknown

Art Unit

2674

CERTIFICATE OF MAILING

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

May 4, 2003

Mark M. Abumeri, Reg. No. 43,458

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application is an Information Disclosure Statement by Applicant (PTO/SB/08 equivalent) listing 68 references to be considered by the Examiner, which includes 21 non-patent literature references. Hardcopies of the 21 non-patent literature references are enclosed herewith.

This Information Disclosure Statement is being filed before the receipt of a first Office Action on the merits, and presumably no fee is required. If a first Office Action on the merits was mailed before the mailing date of this Statement, the Commissioner is authorized to charge the fee set forth in 37 C.F.R. § 1.17(p) to Deposit Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated:

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Mark M. Abumeri

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Attorney of Record

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10/772,120 Application No. INFORMATION DISCLOSURE February 3, 2004 Filing Date Chui, Clarence First Named Inventor ATEMENT BY APPLICANT 2674 Art Unit Unknown. (Multiple sheets used when necessary) Examiner IRDM.002A/IDC-0023 SHEET 1 OF 3 Attorney Docket No.

U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No.	Document Number Number - Kind Code (if known) Example: 1,234,567 B1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear	
	1	2,534,846	12-19-1950	Ambrose et al.		
	2	3,439,973	04-22-1969	Paul et al.		
	3	3,443,854	05-13-1969	Weiss		
	4	3,653,741	04-04-1972	Marks		
	5	3,656,836	04-18-1972	de Cremoux et al.		
	6	3,813,265	05-28-1974	Marks		
	7	3,955,880	05-11-1976	Lierke		
	8	4,099,854	07-11-1978	Decker et al.		
	9	4,228,437	10-14-1980	Shelton		
	10	4,377,324	03-22-1983	Durand et al.		
	11	4,389,096	06-21-1983	Hori et al.		
	12	4,403,248	09-06-1983	te Velde		
	13	4,445,050	04-24-1984	Marks		
	14	4,459,182	07-10-1984	te Velde		
	15	4,519,676	05-28-1985	te Velde		
	16	4,531,126	07-23-1985	Sadones		
	17	4,663,083	05-05-1987	Marks		
	18	4,681,403	07-21-1987	te Velde et al.	·	
	19	4,748,366	05-31-1988	Taylor		
	20	4,786,128	11-22-1988	Birnbach	***************************************	
	21	4,790,635	12-13-1988	Apsley		
	22	4,982,184	01-01-1991	Kirkwood		
	23	5,022,745	06-11-1991	Zahowski et al.		
	24	5,044,736	09-03-1991	Jaskie et al.		
	25	5,075,796	12-24-1991	Schildkraut et al.		
	26	5,078,479	01-07-1992	Vuilleumier		
	27	5,124,834	06-23-1992	Cusano et al.		
	28	5,142,414	08-25-1992	Koehler		
	29	5,153,771	10-06-1992	Link et al.		

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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

T<sup>1</sup> - Place a check mark in this area when an English language Translation is attached.

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/	01	First Named Inventor	Chui, Clarence
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U.S. PATENT DOCUMENTS						
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	30	5,168,406	12-01-1992	Nelson		
	31	5,231,532	07-27-1993	Magel et al.		
	32	5,233,459	08-03-1993	Bozler et al.		
	33	5,311,360	05-10-1994	Bloom et al.		
-	34	5,381,253	01-10-1995	Sharp et al.		
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	44	5,710,656	01-20-1998	Goosen		
	45	5,739,945	04-14-1998	Tayebati		
	46	5,784,190	07-21-1998	Worley		
	47	5,825,528	10-20-1998	Goosen		

	NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>1</sup>		
	48	AKASAKA,"Three-Dimensional IC Trends," Proceedings of IEEE, vol. 74, No. 12, pp. 1703-1714 (Dec. 1986).			
	49	ARATANI et al., "Process and Design Considerations for Surface Micromachined Beams for a Tuneable Interferometer Array in Silicon," Proc. IEEE Microelectromechanical Workshop, Fort Lauderdale, FL, pp. 230-235 (Feb. 1993).			
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	52	GOOSSEN et al., "Possible Display Applications of the Silicon Mechanical Anti-Reflection Switch," Society for Information Display (1994).			

<sup>\*</sup>Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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MAY U 6 2005 10/772,120 Application No. February 3, 2004 INFORMATION DISCLOSURE Filing Date Chui, Clarence First Named Inventor TEMENT BY APPLICANT 2674 Art Unit Unknown. (Multiple sheets used when necessary) Examiner IRDM.002A/IDC-0023 Attorney Docket No. SHEET 3 OF 3

		NON PATENT LITERATURE DOCUMENTS				
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	53	GOOSSEN et al., "Silicon Modulator Based on Mechanically-Active Anti-Reflection Layer with 1Mbit/sec Capability for Fiber-in-the-Loop Applications," IEEE Photonics Technology Letters, pp. 1119-1121 (Sep. 1994).				
	54	GOSCH, "West Germany Grabs the Lead in X-Ray Lithography," Electronics, pp. 78-80 (Feb. 5, 1987).				
	55	HOWARD et al., "Nanometer-Scale Fabrication Techniques," VLSI Electronics: Microstructure Science, vol. 5, pp. 145-153, and pp. 166-173 (1982)				
	56	JACKSON, "Classical Electrodynamics," John Wiley & Sons Inc., pp. 568-573 (date unknown).				
	57	JERMAN et al., "A Miniature Fabry-Perot Interferometer Fabricated Using Silicon Micromaching Techniques," IEEE Electron Devices Society (1998).				
	58	JOHNSON, "Optical Scanners," Microwave Scanning Antennas, vol. 1, pp. 251-261 (1964).				
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	68	WU et al., "Design of a Reflective Color LCD Using Optical Interference Reflectors," ASIA Display '95, pp. 929-931 (Oct. 16, 1995).				

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Examiner Signature Date Considered

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